

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF THE CLAIMS:

1. (Currently Amended) A transceiver interface connectable, in use, to a cable, the interface including an input for receiving a pair of signals from said cable, and a logic circuit connected to the input having a single logic output line, wherein the single logic output line of the logic circuit has a first output state where both signals are below a predetermined level and a second output state where either or both signals exceed the predetermined level and wherein the logic circuit is a NOR gate.

2. (Cancelled)

3. (Previously Presented) An interface as claimed in Claim 1, wherein the first output state is indicative of a single ended zero state.

4. (Previously Presented) An interface as claimed in Claim 1, wherein the second output state is indicative of a non-single ended zero state.

5. (Previously Presented) A serial bus device including a transceiver interface as claimed in claim 1.

6. (Previously Presented) A device as claimed in Claim 5, wherein the device includes an application specific integrated circuit having a single physical connection to said logic output line.

7. (Previously Presented) A device as claimed in Claim 5, wherein the bus is Universal Serial Bus.

8. (Previously Presented) A mobile radio telephone including a device as claimed in Claim 6 as dependent therefrom, wherein the application specific integrated circuit also provides mobile telephony functions.

9. (Cancelled).

10. (Cancelled)

11. (Cancelled)

12. (Previously Presented) An interface as claimed in Claim 3, wherein the second output state is indicative of a non-single ended zero state.

13. (Cancelled)

14. (Previously Presented) A serial bus device including a transceiver interface as claimed in claim 3.

15. (Previously Presented) A serial bus device including a transceiver interface as claimed in claim 4.

16. (Previously Presented) A device as claimed in Claim 6, wherein the bus is Universal Serial Bus.

17. (Previously Presented) A mobile radio telephone including a device as claimed in Claim 7 as dependent therefrom, wherein the application specific integrated circuit also provides mobile telephony functions.

18. (Currently Amended) A transceiver interface connectable, in use, to a cable, the interface including an input for receiving a pair of signals from said cable, and a logic circuit connected to the input having only a single logic output line, wherein the logic circuit has a first output state where both signals are below a predetermined level and a second output state where either or both signals exceed the predetermined level and wherein the logic circuit is a NOR gate.

19. (Cancelled)

20. (Previously Presented) An interface as claimed in Claim 18, wherein the first output state is indicative of a single ended zero state.

21. (Previously Presented) An interface as claimed in Claim 18, wherein the second output state is indicative of a non-single ended zero state.

22. (New) An interface as claimed in Claim 20, wherein the second output state is indicative of a non-single ended zero state.

23. (New) A serial bus device including a transceiver interface as claimed in claim 18.

24. (New) A device as claimed in Claim 23, wherein the device includes an application specific integrated circuit having a single physical connection to said logic output line.

25. (New) A device as claimed in Claim 23, wherein the bus is Universal Serial Bus.

26. (New) A mobile radio telephone including a device as claimed in Claim 24, as dependent therefrom, wherein the application specific integrated circuit also provides mobile telephony functions.